

COMPUTING DEVICE HAVING PROGRAMMABLE STATE TRANSITIONS

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ABSTRACT OF THE DISCLOSURE

A computing device having programmable state transitions is disclosed. The device includes a real-time clock that generates a signal in response to the real-time clock attaining a programmed time of day. The device additionally includes a processor, coupled to the real-time clock that receives the signal and transitions from a first state to a second state, such as from a hibernate state to a standby state.

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